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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s) Epson Research and Development, Inc.

Group Art Unit: 1756

Serial No.: 10/026,286

Examiner: Daborah Chacko Davis

Filed: December 20, 2001

Title: Pattern Forming Method and Apparatus Used for Semiconductor Device, Electric Circuit, Display Module, and Light Emitting Device

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class mail in an envelope addressed to Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this date.

Date: November 22, 2004

Virginia Silva
Virginia Silva

**RESPONSE TO RESTRICTION REQUIREMENT
AND
PETITION FOR EXTENSION OF TIME (IF REQUIRED)**

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Requirement for Restriction in the Office Action dated November 3, 2004, applicant(s) hereby elect without traverse Group I, namely claims 1-47 for prosecution at this time. If necessary, applicants reserve the right to present the remaining claims in a divisional application at the appropriate time.

Applicant(s) believe that no extension of time is required. However, this conditional petition is being made to provide for the possibility that applicant(s) have inadvertently overlooked the need for a petition for extension of time. The Commissioner is hereby authorized to charge payment of the fee for such extension of time to our Deposit Account No. 19-2746.

Respectfully submitted,

Rosalio Haro
Registration No. 42,633

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Date: November 22, 2004